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Patent Application

Inventor(s):

LEE

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Examiner:

Sikha Roy

Title:

MULTI-LAYER ELECTRODE STRUCTURE AND METHOD FOR FORMING
MULTI-LAYER ELECTRODE STRUCTURE FOR A FLAT PANEL DISPLAY
DEVICERECEIVED
JUL 23 2002
TECHNOLOGY CENTER 2800**Form 1449****U.S. Patent Documents**

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
S.R.	A	4,561,009	12/24/85	Yonezawa	357	67	11/22/83
S.R.	B	4,940,916	7/10/90	Borel et al.	313	306	11/3/98
S.R.	C	5,090,932	2/25/92	Dieumegard et al.	445	24	3/24/89
S.R.	D	5,191,217	3/2/93	Kane et al.	250	423F	11/25/91
S.R.	E	5,283,500	2/1/94	Kochanski	315	58	5/28/92
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Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	N							
	O							
	P							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	R	
	S	
	T	
Examiner <i>Sikha Roy</i>		Date Considered <i>8/30/02</i>

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered.
Include copy of this form with next communication to applicant.